

ABSTRACT OF THE DISCLOSURE

A circuit for generating a pixel clock for use in scanning a laser beam includes a high-frequency-clock generating circuit which generates a
5 high-frequency clock having a higher frequency than the pixel clock, and a control circuit which generates the pixel clock while shifting a phase of the pixel clock by a shift step proportional to a clock cycle of the high-frequency clock in response
10 to phase data indicative of timing and amounts of phase shifts.